



**OR**

- b) Design a TTL logic circuit for three input NAND gate. 13,K2,CO2

13. a) (i) Discuss the design procedure for combinational circuit. 5,K2,CO3  
(ii) Design and implement the full adder circuit. 8,K2,CO3

**OR**

- b) Design a BCD to Excess 3 code converter and implement the same using logic gates. 13,K2,CO3

14. a) Explain the circuit of a SR flipflop and explain its operation. Give the truth table, characteristic table and excitation table. 13,K2,CO4

**OR**

- b) A sequential circuit with 2 D FFs A and B and two inputs X and Y, one output Z is specified by the following next state and output equations. 13,K3,CO4

$$A(t+1) = x'y + xA,$$
$$B(t+1) = x'B + xA ; \quad Z = B$$

- (i) Draw the logic diagram of the circuit,  
(ii) Derive the state tables,  
(iii) Draw the state diagram.

15. a) Write the behavioral and structural model of a Full Adder. 13,K3,CO6

**OR**

- b) Discuss in detail different modeling styles of VHDL with suitable example. 13,K3,CO6

**PART - C (1 × 15 = 15 Marks)**

16. a) Design the following using PLA and PROM. 15,K3,CO5  
 $W(A,B,C,D) = \sum m(2,12,13)$   
 $X(A,B,C,D) = \sum m(7,8,9,10,11,12,13,14,15)$   
 $Y(A,B,C,D) = \sum m(0,2,3,4,5,6,7,8,10,11,15)$   
 $Z(A,B,C,D) = \sum m(1,2,8,12,13)$

**OR**

- b) (i) Derive the PLA programming table for the combinational circuit that squares a 3 bit number. 10,K2,CO5  
(ii) Compare three combinational circuits PLA,PAL and ROM. 5,K2,CO5