

Reg. No.		1		

**Question Paper Code** 

11802

## B.E. / B.Tech. - DEGREE EXAMINATIONS, APRIL/MAY 2023

Seventh Semester

## **Electrical and Electronics Engineering EE8701 - HIGH VOLTAGE ENGINEERING**

(Regulations 2017)

Duration: 3 Hours

Max. Marks: 100

## PART - A $(10 \times 2 = 20 \text{ Marks})$

**Answer ALL Questions** 

	This wor The Questions	Marks, K-Level,CO		
1.	Define Isokeraunic level or thunderstorm days.			
2.	What is counter poise wire? Give its use.			
3.	Identify the Town-sends condition for breakdown.			
4.	What are two main reasons for long term break down in composite dielectrics?			
5.	Summarize the specifications for standard impulse wave.			
6.	Outline the drawbacks of single stage circuit for the generation of very high impulse voltage.			
7.				
8.	Define CVT.			
9.	What is meant by insulation coordination?			
10.	. Compare type tests and routine tests.			
	PART - B $(5 \times 13 = 65 \text{ Marks})$ Answer ALL Questions			
11.	a) Discuss the step by step by procedure for constructing Bewley's lattice diagram with an example.  OR	13,K2,CO1		
	b) (i) Explain the mechanism of lightning strokes	8,K2,CO1		
	(ii) Explain the technique of mathematical model of lighting over voltage.	5,K2,CO1		
12.	a) Explain briefly various theories of breakdown in commercial liquid dielectrics.  OR	13,K2,CO2		
	b) Explain the various mechanism of vacuum break down.	13,K2,CO2		

13.	a)	Explain the Marx circuit arrangement for multistage impulse generators. How is the basic arrangement modified to accommodate the wave time control resistances?  OR	13,K2,CO3
	b)	What is the principle behind the electrostatic energy conversion methods? Explain the construction, operation, application and limitation of Van de Graff generator.	13,K2,CO3
14.	a)	Explain briefly the Electrostatic Voltmeter. Also list the advantages and disadvantages.  OR	13,K2,CO4
	b)	With neat sketch explain the sphere gap arrangement method of high voltage measurement and give the factors influencing the measurement.	13,K2,CO4
15.	a)	Demonstrate with neat diagram explain the various HV testing carried out on Insulators.  OR	13,K2,CO5
	b)	Explain briefly the various tests to be carried out on a bushing.	13,K2,CO5
		$PART - C (1 \times 15 = 15 Marks)$	
16	. a)	(i) A Rogowski coil is required to measure impulse current of 8 kA having rate of change of current of 10 <sup>10</sup> A/sec. The voltmeter is connected across the integrating circuit which reads 8 volts for full scale deflection. The input to the integrating circuit is from the Rogowski Coil. Determine the mutual inductance of coil, R and C of the integrating circuit.	8,K2,CO4
	2	(ii) Demonstrate the arrangement and detailed procedure for impulse voltage testing of power transformer.  OR	7,K2,CO5
	b	(i) Compare the performance of capacitance and mixed R-C potential	8,K2,CO4
		dividers for measurement of impulse voltages.  (ii) Discuss the direct testing of circuit breakers in detail.	7,K2,CO5