



SAI RAM ENGINEERING COLLEGE

An Autonomous Institution | Affiliated to Anna University & Approved by AICTE, New Delhi
Accredited by NBA and NAAC "A+" | An ISO 9001:2015 Certified and MHRD NIRF ranked institution
Sai Leo Nagar, West Tambaram, Chennai - 600 044. www.sairam.edu.in



Office of the Controller of Examinations

Lr. No. SEC/CoE/171/2022

25-11-2022

CIRCULAR

CONTINUOUS ASSESSMENT TEST - II - III SEMESTER B.E/B.Tech / M.Tech(CSE)

Continuous Assessment Test -II for all the branches of III Semester B.E/B.Tech and M.Tech(CSE) will be conducted from **02.12.2022** (Friday) to **10.12.2022** (Saturday) with usual vigour and discipline.

Question Paper Pattern:

Part A - $5 \times 2 = 10$ Marks

Part B - $2 \times 13 = 26$ Marks

(Either or pattern with or without sub Divisions)

Part C - $1 \times 14 = 14$ Marks (with or without Choice)

(Based on Application/ Design/ Analysis/ Evaluation/ Creativity/ Case study)

Timings: **10.00 a.m. to 11.30 a.m.**

Course Moderators are requested to send the soft copy of the question bank to qp.coe@sairamtap.edu.in on or before **29.11.2022 (Tuesday)**. The question bank format is attached herewith.

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25/11/22
Controller of Examinations
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(AUTONOMOUS)
CHENNAI-600 044

Principal
26/11/22
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SRI SAIRAM ENGINEERING COLLEGE
West Tambaram, Chennai-44

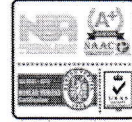
Copy to:

1. Dean Student Affairs
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3. Dean Placement
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TIME TABLE FOR CAT-II EXAMINATIONS (2022-2023 ODD semester)

PROGRAM: B.E - Electrical and
Electronics Engineering

Timing : 10.00 am to 11.30 am

SEM	DATE	DAY	COURSE CODE	COURSE NAME
3	02-Dec-22	Fri	20MGMC301	Constitution of India
3	02-Dec-22	Fri	20EETE301	Live- in - Lab - I
3	05-Dec-22	Mon	20EEPC301	Analog Electronics
3	06-Dec-22	Tue	20EEPC302	DC Machines and Transformers
3	07-Dec-22	Wed	20EEPC303	Electromagnetic theory
3	08-Dec-22	Thu	20EEPC304	Digital Logic Circuits
3	09-Dec-22	Fri	20BSMA301	Linear Algebra, Partial Differential Equations and Transforms

Controller of Examinations

Date: 25-Nov-2022

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