Sri SAI RAM ENGINEERING COLLEGE

An Autonomous Institution | *Affiliated to Anna University & Approved by AICTE, New Delhi*
Accredited by NBA and NAAC "A+" | *An ISO 9001:2015 Certified and MHRD NIRF ranked institution*
Sai Leo Nagar, West Tambaram, Chennai - 600 044. www.sairam.edu.in

Office of the Controller of Examinations

Lr. No. SEC/CoE/157/2022   30-09-2022

CIRCULAR

CONTINUOUS ASSESSMENT TEST - II – V & VII SEMESTER B.E/B.Tech

Continuous Assessment Test –II for all the branches of V and VII Semester B.E/B.Tech will be conducted from **12.10.2022** (Wednesday) to **20.10.2022** (Thursday) with usual vigour and discipline.

Question Paper Pattern:

Part A – 5 × 2 = 10 Marks

Part B– 2 × 13 =26 Marks

(Either or pattern with or without sub Divisions)

Part C– 1 × 14 = 14 Marks (with or without Choice)

(Based on Application/ Design/ Analysis/ Evaluation/ Creativity/ Case study)

Timings: **10.00 a.m. to 11.30 a.m.**

Staff members are requested to submit a set of two question paper per Course. The soft copy of the question papers should be sent to coe@sairam.edu.in and the hard copy should be submitted to the CoE Office on or before **10.10.2022 (Monday)** Course Moderator is responsible in getting the question paper on time.

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Copy to:

1. Dean Student Affairs
2. Dean Academics
3. Dean Placement
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Time Table for CAT-II Examinations (2022-2023 Odd Semester)

PROGRAM: B.E - Electronics and Communication Engineering

Timing : 10.00 am to 11.30 am

SEM	DATE	DAY	COURSE CODE	COURSE NAME
7	14-Oct-22	Fri	EC8701	Antennas and Microwave Engineering
7	15-Oct-22	Sat	EC8702	Ad hoc and Wireless Sensor Networks
7	17-Oct-22	Mon	EC8751	Optical Communication
7	18-Oct-22	Tue	EC8791	Embedded and Real Time Systems
7	19-Oct-22	Wed	20HSMC501	Universal Human Values-II

Controller of Examinations

Date:30-Sep-2022

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Time Table for CAT-II Examinations (2022-2023 Odd Semester)

PROGRAM: B.E - Electronics and Communication Engineering

Timing : 10.00 am to 11.30 am

SEM	DATE	DAY	COURSE CODE	COURSE NAME
5	12-Oct-22	Wed	20ECTE501	Live-in-Lab III (11 am onwards)
5	12-Oct-22	Wed	20ECTP501	Skill Enhancement
5	13-Oct-22	Thu	20ECEL508	Control Systems Engineering
5	15-Oct-22	Sat	20ECPC501	Digital Communication
5	17-Oct-22	Mon	20ECPC502	VLSI Design
5	18-Oct-22	Tue	20ECPW501	Discrete Time Signal Processing with Laboratory
5	19-Oct-22	Wed	20HSMC501	Universal Human Values II

Controller of Examinations

Date: 30-Sep-2022

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