



SAI RAM ENGINEERING COLLEGE

An Autonomous Institution | Affiliated to Anna University & Approved by AICTE, New Delhi
Accredited by NBA and NAAC 'A+' | An ISO 9001:2015 Certified and MHRD NIRF ranked institution
Sai Leo Nagar, West Tambaram, Chennai - 600 044. www.sairam.edu.in



Founder Chairman : MJF. Ln. Leo Muthu

Internal Examination Cell

TIME TABLE FOR CAT-III EXAMINATIONS (2022-2023 ODD semester)

PROGRAM: B.E - Electrical and
Electronics Engineering

Timing : 2.20 pm to 3.50 pm

SEM	DATE	DAY	COURSE CODE	COURSE NAME
3	5-Jan-23	Thu	20MGMC301	Constitution of India
3	5-Jan-23	Thu	20EETP301	Skill Enhancement
3	5-Jan-23	Thu	20EETE301	Live- in - Lab - I
3	6-Jan-23	Fri	20BSMA301	Linear Algebra, Partial Differential Equations and Transforms
3	7-Jan-23	Sat	20EEPC301	Analog Electronics
3	9-Jan-23	Mon	20EEPC302	DC Machines and Transformers
3	10-Jan-23	Tue	20EEPC303	Electromagnetic theory
3	11-Jan-23	Wed	20EEPC304	Digital Logic Circuits

Internal Examination Cell Coordinator

Date: 20-Dec-2022

Principal

Principal

SRI SAIRAM ENGINEERING COLLEGE
West Tambaram, Chennai-44:

21/12/22