



Sri SAI RAM ENGINEERING COLLEGE

An Autonomous Institution | Affiliated to Anna University & Approved by AICTE, New Delhi
Accredited by NBA and NAAC 'A+' | An ISO 9001:2015 Certified and MHRD NIRF ranked institution
Sai Leo Nagar, West Tambaram, Chennai - 600 044. www.sairam.edu.in



OFFICE OF THE CONTROLLER OF EXAMINATIONS
End Semester Examination Time Table Nov / Dec 2020 (May 2021)

Branch : ME – Embedded System Technologies

Sem	Date	Day	Course Code	Course Name	Session
1	06-May-21	Thu	20PESMA102	Applied Mathematics for Electrical Engineers	10:00 am to 11:30 am
1	07-May-21	Fri	20PESPC101	Advanced Digital Principles and Design	10:00 am to 11:30 am
1	08-May-21	Sat	20PESPC102	Microcontroller Based System Design	10:00 am to 11:30 am
1	10-May-21	Mon	20PESPC104	Software for Embedded Systems	10:00 am to 11:30 am
1	11-May-21	Tue	20PESPC103	Design of Embedded Systems	10:00 am to 11:30 am

CONTROLLER OF EXAMINATIONS
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CHENNAI-600 044

Controller of Examinations



Principal
SRI SAI RAM ENGINEERING COLLEGE
West Tambaram, Chennai-44.

Principal