

**Sri Sairam Engineering College**  
**Sai Leo Nagar, West Tambaram, Chennai – 600044**  
**Department of Electronics and Communication Engineering**

**Minutes of the first Board of Studies meeting**

The first Board of Studies (BOS) meeting of the department of ECE was conducted on 04.06.2020 through online mode (ZOOM) at 3.00P.M (IST). Following members were present.

- |                                                                                                                |                                                            |
|----------------------------------------------------------------------------------------------------------------|------------------------------------------------------------|
| <b>1. Dr. D.Sriram Kumar</b><br>Prof. & Head, ECE Dept.<br>National Institute of Technology,<br>Trichirapalli. | <b>External stakeholder</b><br>(Subject Expert)            |
| <b>2. Dr. R.Nakkeeran</b><br>Assoc.Prof., ECE Dept.<br>Pondicherry.                                            | <b>External stakeholder</b><br>(Subject Expert)            |
| <b>3. Dr. S.P.Joy Vasantha Rani</b><br>Prof., Dept. of Electronics Engg.<br>MIT Campus, Anna University.       | <b>External stakeholder</b><br>(University Representative) |
| <b>4. Dr. V.Sapthagirivasan,</b><br>Manager, Capgemini<br>Bengaluru.                                           | <b>External stakeholder</b><br>(Industrial Expert)         |
| <b>5. Dr. S.Lakshmi</b><br>Lead Data Scientist/ BigTapp Analytics<br>Chennai.                                  | <b>External stakeholder</b><br>(Alumni Member)             |
| 6. Dr. S.Brindha                                                                                               | Internal stakeholder                                       |
| 7. Prof. G.Sudha                                                                                               | Internal stakeholder                                       |
| 8. Dr. S.Sumathi                                                                                               | Internal stakeholder                                       |
| 9. Dr. M.Kanthimathi                                                                                           | Internal stakeholder                                       |
| 10. Prof. A.R.Rajini                                                                                           | Internal stakeholder                                       |
| 11. Prof. S.Usha                                                                                               | Internal stakeholder                                       |
| <b>12. Dr J.Raja</b><br>Professor & Head, ECE<br>Sri Sairam Engineering College                                | <b>Chairman, BOS/ECE</b>                                   |

Dr.J.Raja, Chairman of BOS opened the meeting by welcoming and introducing the external members to the internal members. The chairman placed the following agenda for the deliberations

and discussions of the members. The following are the deliberations made during the meeting (as per the items of the circulated agenda).

**BOS.ECE.20.01.01:** To review and approve the curriculum and syllabus of UG programme in ECE. The members recommended the following:

- a. The total cumulative credits of 175 may be reduced to 160 to 165 credits.
- b. Uniformity in the number of text books and reference Books emphasized.
- c. Members asked to ensure that there should not be any overlapping of course / content.
- d. Any non-credit courses can be included to reduce the total number of credits
- e. Dr. D.Sriram Kumar suggested that the course Electronic Circuits in III Semester may be shifted to IV Semester and Electromagnetic Fields to III Semester
- f. Eighth semester must be free of core / elective subjects so that all / interested students can attend internship
- g. Dr.R.Nakkeeran suggested that the non-credit courses can also be included to reduce the total number of credits
- h. For internship, L T P as 0 0 0 with 1 credit can be reconsidered and changed in an effective way - instead of mentioning as 0 0, NA can be included.
- i. Talent Enhancement courses can be encouraged with self-learning mechanisms.
- j. Eighth semester must be free of core / elective subjects so that all / interested students can attend internship.
- k. Dr.S.P.JoyVasantha Rani said that the courses in IV semester are more compared to other semesters which can be reduced.
- l. The subject 'R-Programming and basics of machine learning' and Live in Lab Project-I(Machine Learning) in III semester may be moved to higher semesters.
- m. Live in Lab Project- II(VHDL Synthesis)in IV semester may be moved to III semester if it is related with digital electronics.
- n. Artificial Intelligence Laboratory in VII semester may be added as a Lab component along with the subject 'Artificial Intelligence' in Professional Electives-II.
- o. Dr. S. Lakshmi felt that some subjects are exhaustive, and it is enough that the student may be introduced to the concept.
- p. In Deep learning subject, content of UNIT – III / METRIC learning and its content is questioned
- q. R programming and Machine Learning & Basics of Machine Learning ( III Sem) can be swapped with DSP in V sem as both Signals & Systems and DSP is necessary to understand the basics behind Machine Learning
- r. Dr. V. Sapthagirivasan asked the assessment criteria for Internship which was detailed by the Chairman.
- s. Queried about the syllabus for Wavelet Transform, and asked to consider other frequency domain transformations like ridgelets, curvelets etc.

Based on the suggestions given by the members, the Chairman of BOS told that those fruitful suggestions would be incorporated appropriately in the **Curriculum** and **Syllabi**.

BOS resolved to recommend the following to the Academic Council for further approval.

- a) **Syllabus for core courses of B.E. Electronics and Communication Engineering (ECE) Programme as enclosed in Annexure -I.**
- b) **List of Professional and Open Elective Courses in B.E. Electronics and Communication Engineering (ECE) Programme as enclosed in Annexure -II**

**BOS.ECE.20.01.02:** To review and approve the syllabus of courses offered to **EEE**

The members reviewed the following syllabus of courses offered to **EEE** and resolved to approve the same.

BOS resolved to recommend the following to the Academic Council for further approval.

1. **20ESEC301-Communication Engineering**
2. **20ESEC501-Digital Signal Processing**
3. **20ECPC502-VLSI Design**
4. **20ECEL505-Digital Image Processing**

List of Courses offered for **EEE** department from ECE as enclosed in **Annexure -III.**

**BOS.ECE.20.01.03:** To review and approve the syllabus of courses offered to **EIE**

The members reviewed the following syllabus of courses offered to **EIE** and resolved to approve the same.

BOS resolved to recommend the following to the Academic Council for further approval.

1. **20ESEC301-Communication Engineering**
2. **20ESEC501-Digital Signal Processing**
3. **20ESEC601-VLSI Design**

List of Courses offered for **EIE** department from ECE as enclosed in **Annexure -III.**

**BOS.ECE.20.01.04:** To review and approve the syllabus of courses offered to **ICE**

The members reviewed the following syllabus of courses offered to **ICE** and resolved to approve the same.

BOS resolved to recommend the following to the Academic Council for further approval.

1. **20ESEC301-Communication Engineering**
2. **20ESEC501-Digital Signal Processing**

### 3. 20ESEC601-VLSI Design

List of Courses offered for ICE department from ECE as enclosed in **Annexure -III**.

**BOS.ECE.20.01.05:** To review and approve the syllabus of courses offered to CSE

The members reviewed the following syllabus of courses offered to CSE and resolved to approve the same.

BOS resolved to recommend the following to the Academic Council for further approval.

1. 20ESEC301-Communication Engineering
2. 20ESEC502-Microprocessors and Microcontrollers
3. 20ESPL501–Microprocessor and Microcontrollers Lab

List of Courses offered for CSE department from ECE as enclosed in **Annexure -III**.

**BOS.ECE.20.01.06:** To review and approve the syllabus offered to IT

The members reviewed the following syllabus of courses offered to IT and resolved to approve the same.

BOS resolved to recommend the following to the Academic Council for further approval.

1. 20ESEC301-Communication Engineering
2. 20ESEC502-Microprocessors and Microcontrollers
3. 20ESPL501 –Microprocessor and Microcontrollers Lab

List of Courses offered for the IT department from ECE as enclosed in **Annexure -III**.

**BOS.ECE.20.01.07:** To review and approve the **curriculum and syllabus** of PG Programme in Engineering with specialization in **Communication Systems (CS)**.

The members reviewed the following syllabus of courses offered to **M.E Communication Systems (CS)** and resolved to approve the same. BOS resolved to recommend the following to the Academic Council for further approval.

- A. The members felt that the total credits designed in the PG programme in Engineering with specialization in Communication Systems curriculum is little bit more. The proposed total cumulative credits of 70 may be reduced around 65 credits.
- B. Uniformity in the number of text books and reference books emphasized.

Syllabus for core and professional elective courses of **M.E. Communication Systems** under Electronics and Communication Engineering Programme as enclosed in Annexure -IV.

**BOS.ECE.20.01.08:** To review and approve the curriculum and syllabus of PG programme in Engineering with specialization in Embedded System Technologies (EST).

The members reviewed the following syllabus of courses offered to M.E Embedded System Technologies (EST) and resolved to approve the same.

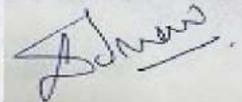
BOS resolved to recommend the following to the Academic Council for further approval.

- A. The members felt that the total credits designed in the PG programme in Engineering with specialization in Embedded System Technologies curriculum is little bit more. The proposed total cumulative credits of 70 may be reduced around 65 credits.
- B. Uniformity in the number of text books and reference books emphasized.

Syllabus for core and professional elective courses of M.E. Embedded System Technologies under Electronics and Communication Engineering Programme as enclosed in Annexure -V.

In the concluding remarks, the Chairman stated that the reduction of credits in UG and PG Curriculum and other recommendations suggested by the members would be considered.

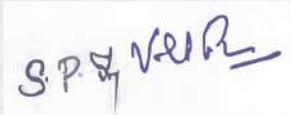
Meeting then ended with a Vote of Thanks by Dr. S.Brindha, Professor/ECE.



**Dr. D.Sriram Kumar**  
Prof. & Head, ECE  
NIT, Tiruchirapalli.



**Dr. R.Nakkeeran**  
Assoc. Prof. & Head, ECE.  
Pondicherry University.



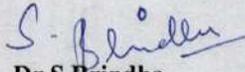
**Dr. S.P.Joy Vasantha Rani**  
Assoc.Prof., Dept. of Electronics  
Engg. MIT Campus, Anna  
University.



**Dr. V.Sathagirivasan**  
Manager, Capgemini  
Bengaluru.



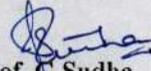
**Dr. S.Lakshmi**  
Lead Data Scientist/ BigTapp Analytics  
Chennai.



**Dr.S.Brindha**

Prof/ ECE

Sri Sairam Engineering College  
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**Prof. G.Sudha**

Assoc. Prof/ ECE

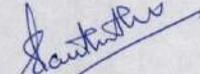
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**Dr. S.Sumathi**

Prof/ ECE

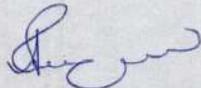
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**Dr. M.Kanthimathi**

Assoc. Prof/ ECE

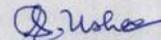
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**Prof. S.Usha**

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**Dr. J.Raja**

Chairman, BoS/ECE  
(Professor & Head/ECE)