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|---|---|----|-----|
| 20. Express the term linear phase response of a filter. | 2 | K2 | CO4 |
| 21. What are the different stages in pipelining? | 2 | K1 | CO5 |
| 22. Name the different buses used in DSP processor. | 2 | K1 | CO5 |

PART - C (6 × 11 = 66 Marks)

Answer ALL Questions

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| 23. a) | A discrete time systems can be | 11 | K2 | CO1 |
| | (i) Static or dynamic | | | |
| | (ii) Linear or non Linear | | | |
| | (iii) Time invariant or time varying | | | |
| | (iv) Stable or unstable | | | |
| | (v) Causal or non causal | | | |
| | Check the following systems with respect to the properties above | | | |
| | $y(n)=x(n)+x(n-1)$. | | | |

OR

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| b) | Check whether the system is periodic and also determine the fundamental period. | 11 | K2 | CO1 |
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$$x(n) = 2\cos\frac{5\pi}{3}n + 3e^{\frac{j3\pi n}{4}}$$

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| 24. a) | Find Fourier Transform of the signal $x(n)=\left(\frac{1}{3}\right)$ for $0\leq n\leq 2$ $x(n)=0$, otherwise. Find the Magnitude and phase for the signal and plot the response. | 11 | K2 | CO2 |
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| b) | Determine the circular convolution of the sequence $x_1(n)=\{1,2,-3,4,-5\}$ and $x_2(n)=\{-2,4,6\}$. | 11 | K2 | CO2 |
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| 25. a) | Compute the DFT of a sequence. $x(n) = \{1,2,1,2,2,1,2,1\}$. | 11 | K3 | CO3 |
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| b) | Compute 8 point FFT of the given sequence using DIT algorithm $x(n)=\{2,2,2,2,1,1,1,1\}$. | 11 | K3 | CO3 |
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| 26. a) | Apply the bilinear transformation for the following:
$H_a(S)=\frac{2}{(s+1)(s+2)}$ with $T=1$ sec find out $H(Z)$. | 11 | K3 | CO4 |
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| b) | The specification of LPF is given by | 11 | K3 | CO4 |
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$$0.8 \leq |H(\omega)| \leq 1 ; \quad 0 \leq \omega \leq 0.2\pi$$

$$|H(\omega)| \leq 0.2; \quad 0.32\pi \leq \omega \leq \pi$$

Design Chebyshev filter using IIT.

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| 27. a) | Explain Von Neumann, Harvard architecture and modified Harvard architecture for the computer. | 11 | K2 | CO5 |
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| b) | Explain the various types of addressing modes of digital signal processor with suitable example. | 11 | K2 | CO5 |
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| 28. a) (i) | Obtain the direct form-I realization for the given difference equation $y(n)=0.5y(n-1)-0.25y(n-2)+x(n)+0.4x(n-1)$. | 6 | K2 | CO4 |
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| (ii) | Draw the architecture of a DSP processor for implementing a DSP algorithm. Explain its features. | 5 | K2 | CO5 |
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OR

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| b) (i) | Explain the design of lowpass digital butterworth filter. | 6 | K2 | CO4 |
| (ii) | Explain the architecture of TMS 320C54X with a neat diagram. | 5 | K2 | CO5 |