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Question Paper Code 12230

## **B.E./B.Tech. - DEGREE EXAMINATIONS, NOV / DEC 2023**

Sixth Semester

# **Electronics and Communication Engineering EC8691 - MICROPROCESSORS AND MICROCONTROLLERS**

(Regulations 2017)

Duration: 3 Hours Max. Marks: 100

# PART - A $(10 \times 2 = 20 \text{ Marks})$

**Answer ALL Questions** 

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		Marks, K-Level, CO
1.	For 8086 microprocessor, the contents of the registers are CS=2001H,	2,K2,CO1
	SS=6046H, IP=2456H, SP=2200H. Calculate the corresponding physical	
	address for the addressed byte in (i) CS (ii) SS.	
2.	Write a short note on Interrupts and Interrupt service routine.	2,K2,CO1
3.	List two differences between minimum mode and maximum mode configuration.	2,K2,CO2
4.	What are the signals used in 8086 Maximum mode operation?	2,K1,CO2
5.	Frame the Control word format to operate 8279 in 8 bit character right entry and encoded scan N-key rollover.	2,K3,CO3
6.	Differentiate the priority schemes available in 8257 DMA Controller.	2,K2,CO3
7.	Specify the RAM address location allotted for the SFRs TL0, DPH, IE and P2.	2,K2,CO4
8.	Why pull up resistor has to be externally interfaced with port 0?	2,K1,CO4
9.	Write an ALP to receive input from port P1.5 and if it is high then an output 35H is sent to port 0.	2,K3,CO5
10.	What are the bits available in TMOD register?	2,K1,CO5

### PART - B $(5 \times 13 = 65 \text{ Marks})$

**Answer ALL Questions** 

11. a) Explain the Internal Architecture of 8086 with neat diagram. 13,K2,CO1

#### OR

- b) How are 8086 instructions classified according to their functional 13,K2,CO1 categories? Give example.
- 12. a) Describe the minimum mode 8086 system and its timing diagram 13,K2,CO2 Minimum Mode.

**OR** 

b) Write an ALP for finding largest and smallest numbers in an array of 13,K2,CO2 numbers. 13.K2.CO3 13. With a neat diagram discuss the various modes of operation of 8255. a) OR 13,K2,CO3 Explain how serial communication can be established in 8086 using b) 8251 IC. 14. Explain the architecture of the 8051 microcontroller with a neat 13.K2.CO4 a) diagram. OR 6.K2.CO4 b) (i) Enumerate the ports available in 8051. (ii) Describe the memory organization in 8051 and Explain how 7,K2,CO4 external memory is interfaced with 8051. 13,K2,CO5 15. Examine the different modes of operation of timers/counters in a) 8051Microcontroller with its associated register. OR Develop an ADC and DAC interface with 8051 along with program. 13.K2.CO5 b) PART - C  $(1 \times 15 = 15 \text{ Marks})$ 9,K3,CO6 16. (i) Explain the working of a common cathode type LED seven a) segment displays. (ii) Discuss the working of an Alarm Controller. 6,K2,CO6 OR With neat sketch and program, explain the microprocessor based 15,K2,CO6 b) traffic light control system.